

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Inventor(s) : Harald GUENSCHER et al.  
Serial No. : 10/540,653  
Filed : February 7, 2006  
For : METHOD FOR ADJUSTING THE ELECTRICAL  
RESISTANCE OF A RESISTOR RUN  
Examiner : Thiem D. Phan  
Art Unit : 3729  
Confirmation No. : 7204

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Commissioner for Patents  
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I hereby certify that this correspondence is being electronically transmitted to the United States Patent and Trademark Office via the Office electronic filing system on **February 5, 2009**.

Signature: /Wendy Espinal/  
Wendy Espinal

**REPLY UNDER 37 C.F.R. § 1.116**

S I R:

In response to the Final Office Action of December 5, 2008 (the three-month response date for which is March 5, 2009), kindly amend the above-captioned application without prejudice as follows:

**Remarks** begin on page 2 of this paper.

While no fee is believed to be due, the Commissioner is authorized, as appropriate and/or necessary, to charge any fees (including any Rule 136(a) extension fees) or credit any overpayment to Deposit Account No. **11-0600** of Kenyon & Kenyon LLP.